

An IATF 16949, ISO9001 and ISO 14001 Certified Company





POWER TRANSISTORS

TIP41, A, B, C NPN TIP42, A, B, C PNP



TO-220 Leaded Plastic Package RoHS compliant

TO-220

APPLICATIONS:

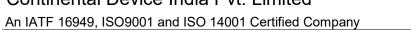
- 1. Complementary Silicon Transistors intended for a wide variety of Switching and Amplifier Applications
- 2. Series and Shunt Regulators
- 3. Driver and Output stages of Hi-Fi Amplifiers

ABSOLUTE MAXIMUM RATINGS (T_a = 25 °C)

DESCRIPTION		SYMBOL	TIP41	TIP41A	TIP41B	TIP41C	LINIT	
			TIP42	TIP42A	TIP42B	TIP42C	UNIT	
Collector Emitter Voltage		V_{CEO}	40	60	80	100	V	
Collector Base Voltage		V_{CBO}	40	60	80	100	V	
Emitter Base Voltage		V_{EBO}	5				V	
Collector Current Continue	ous	I _C	6			Α		
Collector Current Peak		I _{CM}	10				Α	
Base Current		I _B	2				Α	
Power Dissipation upto	T _c =25°C	D	65			W		
Fower Dissipation upto	Derate above 25°C	P_{D}		mW/ºC				
Dower Dissipation unto	T _a =25°C	D	2			W		
Power Dissipation upto	Derate above 25°C	P _D	16			mW/ºC		
Unclamped Inductive Load Energy		*E	62.5			mJ		
Storage Temperature		T _{stg}	150			°C		
Junction Temperature		Tj	- 65 to +150			°C		
THERMAL RESISTANCE								
Junction to Case		R _{θ (j-c)}	1.92			°C/W		
Junction to Ambient in free air		$R_{\theta (j-a)}$	62.5			°C/W		

^{*} I_c=2.5A, L=20mH, P.R.F.=10Hz, V_{cc}=10V, R_{BE}=100fi









ELECTRICAL CHARACTERISTICS at $T_a = 25 \, ^{\circ}\text{C}$

DESCRIPTION		SYMBOL	TEST CONDITION	MIN	MAX	UNIT
Collector Emitter Voltage	TIP41/42	*\/		40		V
	TIP41A/42A		I = 20m A I = 0	60		V
	TIP41B/42B	*V _{CEO}	I_C =30mA, I_B =0	80		V
	TIP41C/42C			100		V
Collector Cut off Current	TIP41, A / 42, A	- I _{CEO}	V_{CE} =30V, I_{B} =0		0.7	mA
Collector Cut on Current	TIP41B, C / 42B, C		V_{CE} =60V, I_{B} =0		0.7	mA
Collector Cut off Current		I _{CES}	$V_{CE}=V_{CEO(max)},$ $V_{BE}=0$		0.4	mA
Emitter Cut off Current		I _{EBO}	V_{EB} =5 V , I_{C} =0		1	mA
DC Comment California		*h _{FE}	I_{C} =0.3A, V_{CE} =4V	30		
DC Current Gain	DC Current Gain		$I_C=3A$, $V_{CE}=4V$	15	75	
Collector Emitter Saturatio	n Voltage	*V _{CE (sat)}	$I_C = 6A, I_B = 0.6A$		1.5	V
Base Emitter on Voltage		$^*V_{BE(on)}$	$I_C=6A, V_{CE}=4V$		2	V
DYNAMIC CHARACTERI	STIC					
DESCRIPTION		SYMBOL	TEST CONDITION	MIN	MAX	UNIT
Small Signal Current Gain		h _{fe}	I_C =0.5A, V_{CE} =10V, f=1KH _z	20		
Transition Frequency		f _T	I _C =0.5A, V _{CE} =10V, f=1MH _z	3		MHz
SWITCHING CHARACTERISTICS						
DESCRIPTION		SYMBOL	TEST CONDITION	TYP		UNIT
Turn On Time		t _{on}	V _{cc} =30V, I _c =6A,		.6	ms
Turn Off Time		t _{off}	$I_{B1} = I_{B2} = 0.6A, R_L = 5W$	1.4		ms

^{*}Pulse Test : Pulse width ≤300ms, Duty Cycle ≤2%



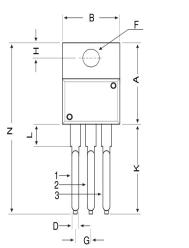


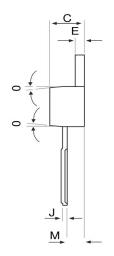




PACKAGE DETAILS

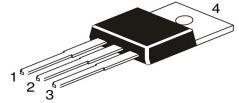
Package: TO-220





DIM	MIN	MAX			
Α	14.42	16.51			
В	9.63	10.67			
С	3.56	4.83			
D	_	0.90			
E	1.15	1.40			
F	3.75	3.88			
G	2.29	2.79			
Н	2.54	3.43			
J	_	0.56			
K	12.70	14.73			
L	2.80	4.07			
М	2.03	2.92			
N	_	31.24			
0	7 DEG				

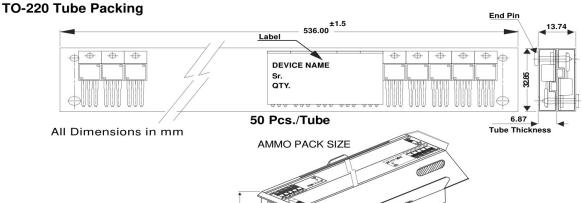
All diminsions in mm.



Pin Configuration

20 Tubes/Ammo Pack 1000 Pcs./Ammo Pack

- 1. Base
- 2. Collector
- 3. Emitter
- 4. Collector

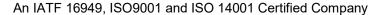


Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	GrWt
TO-220 /FP	200 pcs/polybag	396 gm/200 pcs	3'x7.5'x7.5'	1.0K	17'x15'x13.5'	16.0K	36 kgs
	50 pcs/tube	120 gm/50 pcs	3.5'x3.7'x21.5'	1.0K	19' x 19' x 19'	10.0K	29 kgs

TIP41, A,B,C TIP42, A, B, C Rev1_21022020EM







Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- · Temperature 5 °C to 30 °C
- · Humidity between 40 to 70 %RH
- · Air should be clean.
- · Avoid harmful gas or dust.
- · Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- · Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- · Avoid rapid change of temperature.
- · Avoid condensation.
- · Mechanical stress such as vibration and impact shall be avoided.
- · The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level					
Level	Time	Condition			
1	Unlimited	≤30 °C / 85% RH			
2	1 Year	≤30 °C / 60% RH			
2a	4 Weeks	≤30 °C / 60% RH			
3	168 Hours	≤30 °C / 60% RH			
4	72 Hours	≤30 °C / 60% RH			
5	48 Hours	≤30 °C / 60% RH			
5a	24 Hours	≤30 °C / 60% RH			
6	Time on Label(TOL)	≤30 °C / 60% RH			

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Customer Notes

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered trademark of

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